

ABSTRACT OF THE DISCLOSURE

A processor may include a processor core and at least one peripheral device. A selecting circuit may be used for determining an operational state of the processor and for outputting a selection signal based on the evaluation. A high-speed control circuit may be used for controlling high-speed operations of at least one of the processor core and the peripheral device in response to the selection signal, and a low-speed and low-power control circuit may be used for controlling low-speed and low-power operations of at least one of the processor core and the peripheral device in response to the selection signal.